

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10583538			
<b>Filing Date:</b>				
<b>Title of Invention:</b>	Noise-reducing transistor arrangement, integrated circuit, and method for reducing the noise of field effect transistors			
<b>First Named Inventor/Applicant Name:</b>	Ralf Brederlow			
<b>Filer:</b>	Laura C. Brutman/Inga Hildreth			
<b>Attorney Docket Number:</b>	V0195.0080			
Filed as Large Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
Claims in excess of 20	1615	11	50	550
<b>Miscellaneous-Filing:</b>				
Oath/decl > 30 months from priority date	1617	1	130	130
Eng. Transl. > 30 mo. from priority date	1618	1	130	130
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				810